

**AMENDMENTS TO THE CLAIMS**

**This listing of claims will replace all prior versions and listings of claims in the application:**

**LISTING OF CLAIMS:**

1-3. (Canceled).

4. (Currently amended):An information processing terminal system comprising:  
an information processing terminal; and  
a transmitting and receiving unit which can be attached to or detached from said  
information processing terminal,  
wherein said transmitting and receiving unit comprises:  
a transmission and reception processing section;  
a demodulation section;  
a modulation section and a baseband processing section,  
when said transmitting and receiving unit is attached to said information processing  
terminal, said transmission and reception processing section outputs a reception modulation  
wave signal from a network to said demodulation section and transmits a transmission  
modulation wave signal from said modulation section to the network,  
said demodulation section converts the reception modulation wave signal from said  
transmission and reception processing section into a reception analog baseband signal,  
said baseband processing section converts the reception analog baseband signal into a  
reception digital signal to output to said information processing terminal, and converts a

transmission digital signal from said information processing terminal into a transmission analog baseband signal,

said modulation section converts the transmission analog baseband signal into the transmission modulation wave signal,

said baseband processing section and said information processing terminal operate in synchronization with a clock, and

the reception digital signal contains a reception data, and the transmission digital signal contains a transmission data,

wherein said baseband processing section converts the reception analog baseband signal into a reception digital baseband signal as the reception digital signal to output to said information processing terminal, and converts a transmission digital baseband signal as the transmission digital signal from said information processing terminal into the transmission analog baseband signal,

said information processing terminal converts the reception digital baseband signal from said baseband processing section into the reception data and converts the transmission data into the transmission digital baseband signal, and

~~The information processing terminal system according to claim 2,~~ wherein said information processing terminal comprises:

an interface; and

a control unit configured to convert the reception digital baseband signal supplied through said interface from said baseband processing section into the reception data, and to convert the transmission data into said transmission digital baseband signal to output to said baseband processing section through said interface,

said transmitting and receiving unit further comprises a clock generator,  
said demodulation section generates and outputs a reception symbol clock having a frequency to said clock generator,  
said clock generator generates a second reception symbol clock based on the reception symbol clock from said demodulation section to output to said baseband processing section, said interface and said control unit as a clock, and  
the second reception symbol clock is synchronous with the reception symbol clock and has a frequency different from a frequency of the reception symbol clock.

5. (Currently amended): An information processing terminal system comprising:  
an information processing terminal; and  
a transmitting and receiving unit which can be attached to or detached from said information processing terminal,  
wherein said transmitting and receiving unit comprises:  
a transmission and reception processing section;  
a demodulation section;  
a modulation section and a baseband processing section,  
when said transmitting and receiving unit is attached to said information processing terminal, said transmission and reception processing section outputs a reception modulation wave signal from a network to said demodulation section and transmits a transmission modulation wave signal from said modulation section to the network,  
said demodulation section converts the reception modulation wave signal from said transmission and reception processing section into a reception analog baseband signal,

said baseband processing section converts the reception analog baseband signal into a reception digital signal to output to said information processing terminal, and converts a transmission digital signal from said information processing terminal into a transmission analog baseband signal,

said modulation section converts the transmission analog baseband signal into the transmission modulation wave signal,

said baseband processing section and said information processing terminal operate in synchronization with a clock, and

the reception digital signal contains a reception data, and the transmission digital signal contains a transmission data,

wherein said baseband processing section converts the reception analog baseband signal into a reception digital baseband signal as the reception digital signal to output to said information processing terminal, and converts a transmission digital baseband signal as the transmission digital signal from said information processing terminal into the transmission analog baseband signal,

said information processing terminal converts the reception digital baseband signal from said baseband processing section into the reception data and converts the transmission data into the transmission digital baseband signal, and

~~The information processing terminal system according to claim 2,~~ wherein said information processing terminal comprises:

an interface; and

a control unit configured to convert the reception digital baseband signal supplied through said interface from said baseband processing section into the reception data; and to

convert the transmission data into the transmission digital baseband signal to output to said baseband processing section through said interface; and

a clock generator,

said demodulation section generates and outputs a reception symbol clock having a frequency to said baseband processing section, said interface and said clock generator as a clock,

said clock generator receives the reception symbol clock from the demodulation section as a first clock, generates and outputs a second clock synchronous with the first clock to said control unit as a clock, and generates the second clock through self-oscillation to output to said control unit as a clock, when the first clock is not supplied.

6-10. (Canceled).

11. (Original): An information processing terminal system comprising:  
an information processing terminal; and  
a transmitting and receiving unit which can be attached to or detached from said  
information processing terminal,  
wherein said transmitting and receiving unit comprises:  
a transmission and reception processing section;  
a demodulation section;  
a modulation section and a baseband processing section,  
when said transmitting and receiving unit is attached to said information processing  
terminal, said transmission and reception processing section outputs a reception modulation

wave signal from a network to said demodulation section and transmits a transmission modulation wave signal from said modulation section to the network,

said demodulation section converts the reception modulation wave signal from said transmission and reception processing section into a reception analog baseband signal,

said baseband processing section converts the reception analog baseband signal into a reception digital signal to output to said information processing terminal, and converts a transmission digital signal from said information processing terminal into a transmission analog baseband signal,

said modulation section converts the transmission analog baseband signal into the transmission modulation wave signal,

said baseband processing section and said information processing terminal operate in synchronization with a clock, and

the reception digital signal contains a reception data, and the transmission digital signal contains a transmission data,

wherein said baseband processing section converts the reception analog baseband signal into the reception data as the reception digital signal to output to said information processing terminal and converts the transmission data as the transmission digital signal from said information processing terminal into the transmission analog baseband signal, and

~~The information processing terminal system according to claim 9,~~ wherein said information processing terminal comprises:

an interface; and

a control unit configured to receive the reception data through said interface from said baseband processing section and to output the transmission data to said baseband processing section through said interface,

said transmitting and receiving unit further comprises a clock generator,

said demodulation section generates and outputs a reception symbol clock having a frequency to said clock generator,

said clock generator generates a second reception symbol clock based on the reception symbol clock from said demodulation section to output to said baseband processing section, said interface and said control unit as a clock, and

said second reception symbol clock is synchronous with the reception symbol clock and has a frequency different from the frequency of the reception symbol clock.

12. (Original): An information processing terminal system comprising:  
an information processing terminal; and  
a transmitting and receiving unit which can be attached to or detached from said  
information processing terminal,  
wherein said transmitting and receiving unit comprises:  
a transmission and reception processing section;  
a demodulation section;  
a modulation section and a baseband processing section,  
when said transmitting and receiving unit is attached to said information processing  
terminal, said transmission and reception processing section outputs a reception modulation

wave signal from a network to said demodulation section and transmits a transmission modulation wave signal from said modulation section to the network,

said demodulation section converts the reception modulation wave signal from said transmission and reception processing section into a reception analog baseband signal,

said baseband processing section converts the reception analog baseband signal into a reception digital signal to output to said information processing terminal, and converts a transmission digital signal from said information processing terminal into a transmission analog baseband signal,

said modulation section converts the transmission analog baseband signal into the transmission modulation wave signal,

said baseband processing section and said information processing terminal operate in synchronization with a clock, and

the reception digital signal contains a reception data, and the transmission digital signal contains a transmission data,

wherein said baseband processing section converts the reception analog baseband signal into the reception data as the reception digital signal to output to said information processing terminal and converts the transmission data as the transmission digital signal from said information processing terminal into the transmission analog baseband signal, and

~~The information processing terminal system according to claim 9,~~ wherein said information processing terminal comprises:

an interface;



a control unit configured to receive the reception data through said interface from said baseband processing section and to output the transmission data to said baseband processing section through said interface; and

a clock generator,

said demodulation section generates and outputs a reception symbol clock having a frequency to said baseband processing section, said interface and said clock generator as the clock, and

said clock generator receives the reception symbol clock from said demodulation section as a first clock, generates and outputs a second clock synchronous with the first clock to said control unit as a clock, and generates the second clock through self-oscillation to output to said control unit as the clock when the first clock is not received.

13-50. (Canceled).